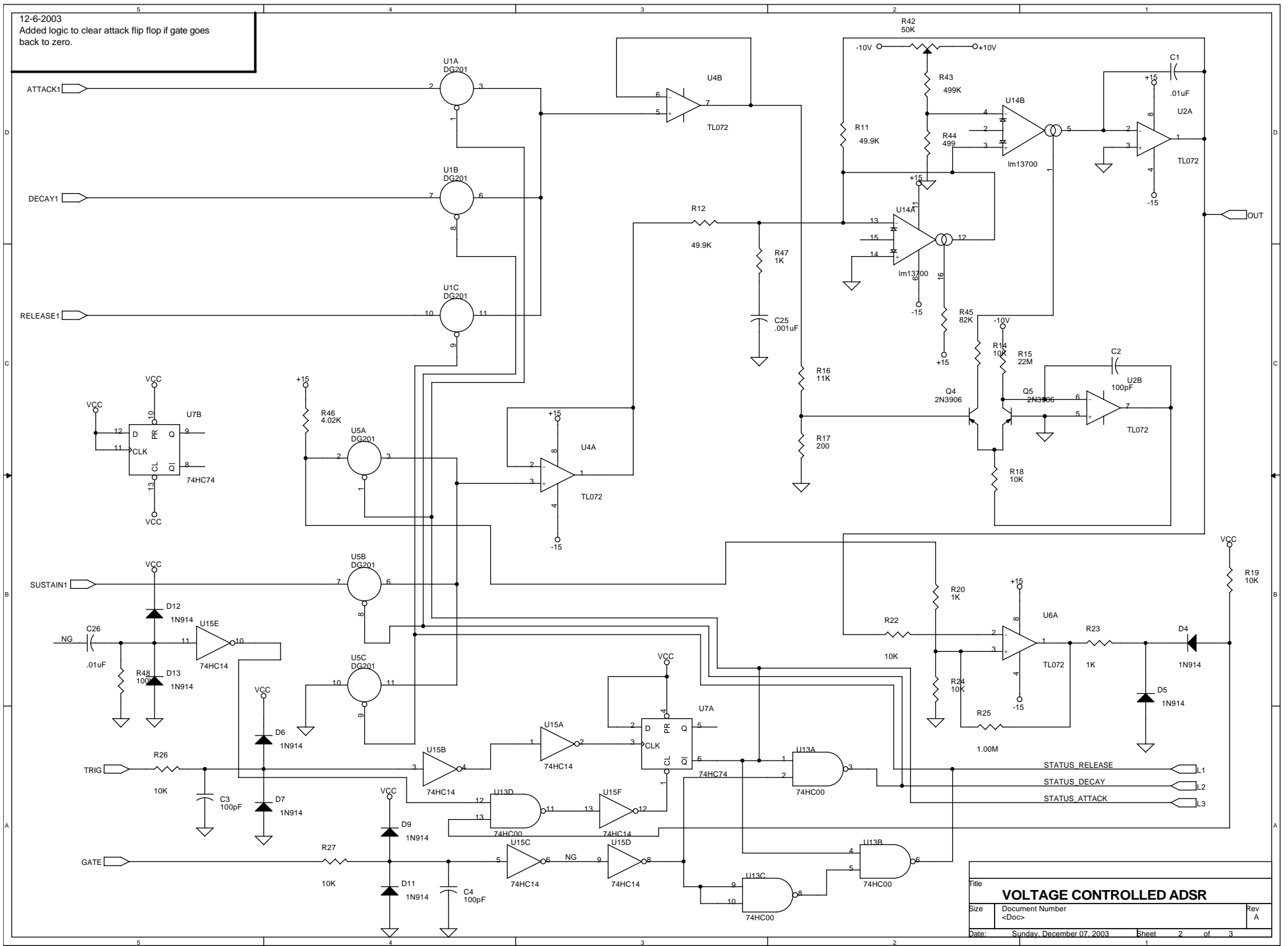
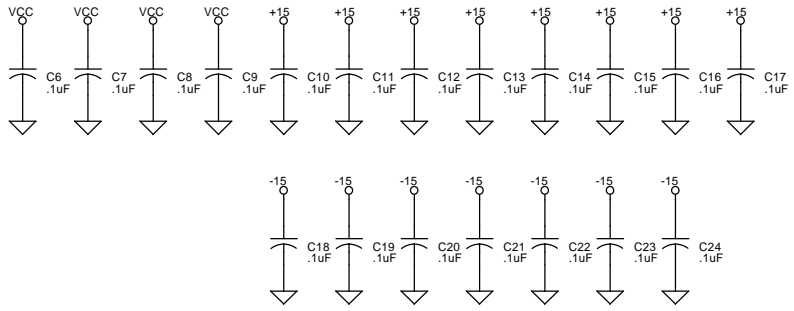
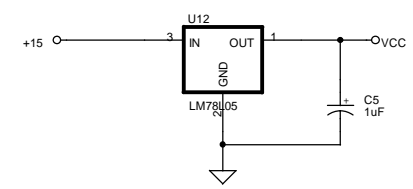
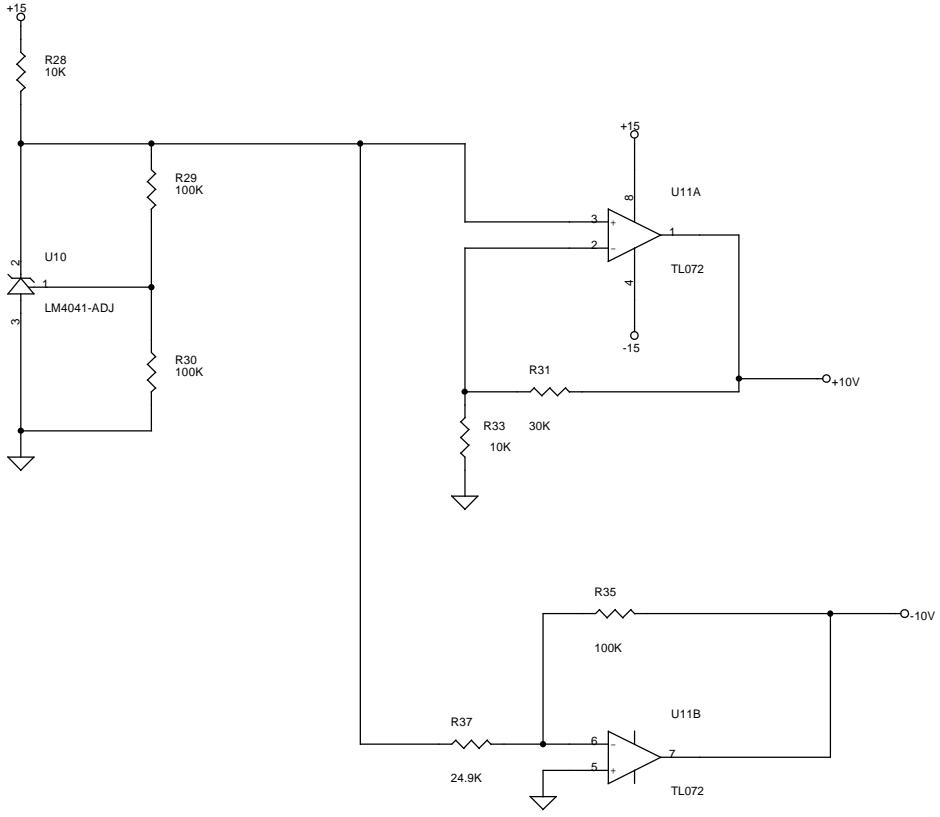


Title		
VOLTAGE CONTROLLED ADSR		
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	100-1004	A
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12-6-2003
 Added logic to clear attack flip flop if gate goes back to zero.



Title		
VOLTAGE CONTROLLED ADSR		
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	<Doc>	A
Date:	Sunday, December 07, 2003	Sheet 2 of 3



Title		
VOLTAGE CONTROLLED ADSR		
Size	Document Number	Rev
Custom>Doc>		A
Date:	Wednesday, June 25, 2003	Sheet 3 of 3

1: VOLTAGE CONTROLLED ADSR Revised: Wednesday, June 25, 2003
 2: 100-1004 Revision: A
 3:
 4: Jim Patchell
 5: patchell@silcom.com
 6:
 7:
 8:
 9:

10: Bill Of Materials September 18,2003 10:24:59 Page1

11:	12: Item	Quantity	Reference	Part
13:	<hr/>			
14:				
15:	1	1	C1	.01uF
16:	2	3	C2,C3,C4	100pF
17:	3	1	C5	1uF
18:	4	19	C6,C7,C8,C9,C10,C11,C12,	.1uF
19:			C13,C14,C15,C16,C17,C18,	
20:			C19,C20,C21,C22,C23,C24	
21:	5	1	C25	.001uF
22:	6	3	D1,D2,D3	LED
23:	7	6	D4,D5,D6,D7,D9,D11	1N914
24:	8	4	J1,J3,J4,J5	PHONEJACK
25:	9	1	P1	POWER
26:	10	5	Q1,Q2,Q3,Q4,Q5	2N3906
27:	11	4	R1,R4,R6,R9	POT
28:	12	3	R2,R5,R8	22K
29:	13	4	R3,R7,R10,R44	499
30:	14	2	R12,R11	49.9K
31:	15	9	R14,R18,R19,R22,R24,R26,	10K
32:			R27,R28,R33	
33:	16	1	R15	22M
34:	17	1	R16	11K
35:	18	1	R17	200
36:	19	6	R20,R23,R39,R40,R41,R47	1K
37:	20	1	R25	1.00M
38:	21	3	R29,R30,R35	100K
39:	22	1	R31	30K
40:	23	1	R37	24.9K
41:	24	1	R42	50K
42:	25	1	R43	499K
43:	26	1	R45	82K
44:	27	1	R46	4.02K
45:	28	2	U1,U5	DG201
46:	29	4	U2,U4,U6,U11	TL072
47:	30	1	U7	74HC74
48:	31	1	U10	LM4041-ADJ
49:	32	1	U12	LM78L05
50:	33	1	U13	74HC00
51:	34	1	U14	lm13700
52:	35	1	U15	74HC14
53:				